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U.S. PATENT AND TRADEMARK OFFICE
GROUP 2811

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

#30 | H (HE) CHAMPS
7/11/2003

IN RE APPLICATION OF :
HIDEKI TAKAHASHI : EXAMINER: LOKE, STEVEN
SERIAL NO: 09/421,217 :
2ND RCE FILED: MARCH 6, 2003 : GROUP ART UNIT: 2811
FOR: INSULATED GATE :
SEMICONDUCTOR DEVICE WITH :
LOW ON VOLTAGE AND :
MANUFACTURING METHOD :
THEREOF

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AMENDMENT

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated September 24, 2003, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.